

**Amendments to the Specification:**

Please amend the section entitled "Related Applications" found on page 2 of the Specification to read as follows:

**RELATED APPLICATIONS**

This patent application is related to U.S. Patent [Application Serial No. 09/754,684] No. 6,662,200, titled "Multiplierless Pyramid Filter," [filed January 3, 2001] issued December 9, 2003, by Tinku Acharya, assigned to the assignee of the present invention and herein incorporated by reference.

Please replace the Abstract found on page 24 with the following:

Implementations of a two-dimensional pyramid filter are disclosed including a two-dimensional pyramid filter architecture of order  $2N-1$  to receive input signals, where  $N$  is a positive integer greater than two. The two-dimensional pyramid filter architecture of order  $2N-1$  includes one-dimensional pyramid filters of order  $2N-1$  and two-dimensional pyramid filters of order  $[2(N-1)-1]$ . The two dimensional pyramid filter architecture of order  $2N-1$  is capable of producing, in operation, on respective clock cycles, at least pyramid filtered output signals corresponding to output signals produced by two one-dimensional pyramid filters of order  $2N-1$  and pyramid filtered output signals corresponding to output signals produced either by four two-dimensional pyramid filters or one two-dimensional pyramid filter of order  $[2(N-1) - 1]$ . The respective output signals in the two dimensional pyramid filter architecture are summed by a summer circuit on respective clock cycles of the two dimensional pyramid filter architecture.